Amendment to the Claims:

This listing of claims 1-12 will replace all prior versions, and listing of claims in the application.

Listing of Claims

- 1. (Currently Amended) System for <u>reading rendering</u> information from a record carrier, the system comprising:
- a record carrier comprising a track for carrying marks representing the information, and
 - a device for reading the information from the track,
 - which record carrier comprises:
- (a) an integrated circuit comprising communication means for contactlessly communicating with the device and
- **(b)** a power supply coil for generating supply power from low frequency magnetic flux changes, and

which device comprises:

- (a) a head for reading the marks,
- (b) communication means for contactlessly communicating with the integrated circuit,
- (c) a magnetic array having a plurality of poles for generating a static magnetic field having <u>flux</u> areas with positive and negative magnetic flux for **detection by cooperating with** the power supply coil, and
- (d) drive means for rotating the record carrier for scanning the track via the head and for moving the integrated circuit through the magnetic field for creating the low frequency magnetic flux changes.

2. (Currently Amended) **A device Device** for reading the information from a track of a record carrier,

which record carrier comprises:

- (a) an integrated circuit comprising communication means for contactlessly communicating with the device and
- (b) a power supply coil for generating supply power from low frequency magnetic flux changes,

which device comprises:

- (a) a head for reading the marks,
- (b) communication means for contactlessly communicating with the integrated circuit,
- (c) a magnetic array having a plurality of poles for generating a static magnetic field having flux areas with positive and negative magnetic flux for detection by cooperating with the power supply coil, and
- (d) drive means for rotating the record carrier for scanning the track via the head and for moving the integrated circuit through the flux areas for creating the low frequency magnetic flux changes.
- 3. (Original) Device as claimed in claim 2, wherein the magnetic array is arranged to create the flux areas having a dimension in the direction of said movement of the integrated circuit of the same order of magnitude as the dimension of the integrated circuit.
- 4. (Original) Device as claimed in claim 2, wherein the magnetic array is arranged to create the flux areas only along a circle segment substantially smaller than the full circle of the path traveled by the integrated circuit during said movement.

- 5. (Original) Device as claimed in claim 2, wherein the magnetic array comprises a plurality of rectangular magnets fitted in a magnetically conducting rail.
- 6. (Original) Record carrier comprising a track for carrying marks representing information, which record carrier comprises an integrated circuit comprising communication means for contactlessly communicating with a device and a power supply coil for generating supply power from low frequency magnetic flux changes created by moving the integrated circuit through a magnetic field having flux areas with positive and negative magnetic flux.
- 7. (Original) Record carrier as claimed in claim 6, wherein the track is arranged on a conductive recording layer, and said layer is interrupted in an annular area containing the integrated circuit.
- 8. (Original) Integrated circuit, which integrated circuit comprises communication means for contactlessly communicating with a device and a power supply coil for generating supply power from low frequency magnetic flux changes created by moving the integrated circuit through a magnetic field having flux areas with positive and negative magnetic flux.
- 9. (Original) Integrated circuit as claimed in claim 8, wherein the power supply coil has at least 10 windings.
- 10. (Original) Integrated circuit as claimed in claim 8, wherein the integrated circuit comprises a speed detection unit for detecting the speed of a movement of the integrated circuit through a magnetic field having flux areas with positive and negative magnetic flux.